

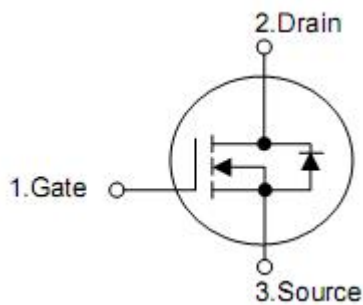
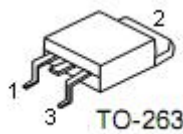
1. Features

- Uses CRM(CQ)advanced Trench technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent $Q_g \times R_{DS(on)}$ product(FOM)
- Qualified according to JEDEC criteria

2. Features

- $R_{DS(on)}=7m\Omega$ (typ.)@ $V_{GS}=10V$
- Motor control and drive
- Battery management
- UPS (Uninterruptible Power Supplies)

3. Pin configuration



Pin	Function
1	Gate
2	Drain
3	Source

4. Ordering Information

Part Number	Package	Brand
KNB3112A	TO-263	KIA

5. Absolute maximum ratings

TC=25 °C unless otherwise specified

Parameter	Symbol	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}	120	V
Continuous Drain Current	I _D	T _C =25 °C	110
		T _C =100 °C	70
Pulsed drain current (T _C = 25°C, t _p limited by T _{jmax})	I _{DP}	440	A
Avalanche energy, single pulse (L=0.5mH, Rg=25Ω)	E _{AS}	256	mJ
Gate-Source voltage	V _{GS}	±25	V
Power dissipation (TC = 25 °C)	P _D	254	W
Junction & Storage Temperature Range	T _J & T _{STG}	-55 to 150	°C
Soldering temperature, wave soldering only allowed at leads (1.6mm from case for 10s)	T _{sold}	260	°C

6. Thermal characteristics

Parameter	Symbol	Ratings	Units
Thermal resistance, junction-ambient ¹	R _{θJA} ²	91	°C/W
Thermal resistance, Junction-case ¹	R _{θJC}	0.49	

7. Electrical characteristics

(T_J=25°C, unless otherwise notes)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	120	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =120V, V _{GS} =0V, T _j =25 °C	-	-	1	μA
		V _{DS} =120V, V _{GS} =0V, T _j =150 °C	-	-	200	
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2	3	4	V
Gate leakage current	I _{GSS}	V _{GS} =±25V, V _{DS} =0V	-	±10	±100	nA
Drain-source on-resistance ²	R _{DS(on)}	V _{GS} =10V, I _D =30A, T _j =25 °C	-	7	10	mΩ
Forward Transconductance	g _{fs}	V _{DS} =5V, I _D =75A	-	150	-	S
Dynamic characteristics						
Gate Resistance	R _G	V _{GS} =0V, V _{DS} =0V Frequency=1MHz	-	1.5	-	Ω
Input capacitance	C _{iss}	V _{DS} =60V, V _{GS} =0V, F=1MHz	-	6000	-	pF
Output capacitance	C _{oss}		-	412	-	pF
Reverse transfer capacitance	C _{rss}		-	130	-	pF
Turn-on delay time	t _{d(on)}		V _{DD} =60V, I _D =75A, V _{GS} =10V, R _G =2.7Ω	-	20	-
Rise time	t _r	-		103	-	ns
Turn-off delay time	t _{d(off)}	-		62	-	ns
Fall time	t _f	-		110	-	ns
Gate Charge Characteristics						
Total gate charge	Q _g	V _{DS} =60V, I _D =75A, V _{GS} =10V, F=1MHz	-	128	-	nC
Gate-source charge	Q _{gs}		-	30	-	nC
Gate-drain charge	Q _{gd}		-	46	-	nC
Diode characteristics						
Diode forward voltage ²	V _{SD}	V _{GS} =0V, I _{SD} =75A	-	0.9	1.3	V
Body Diode Continuous Forward Current ²	I _S	T _C =25 °C	-	-	110	A
Reverse recovery time	t _{rr}	I _F =75A	-	60	-	ns
Reverse recovery charge	Q _{rr}	DI _F /dt=100A/μs	-	150	-	nC

Note: 1. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.

2. The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%

3. The value of R_{thJA} is measured by placing the device in a still air box which is one cubic foot.

8. Typical Characteristics

Fig 1: Output Characteristics

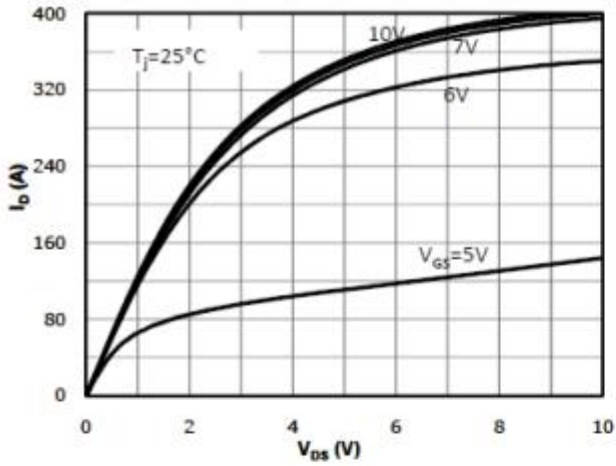


Fig 2: Transfer Characteristics

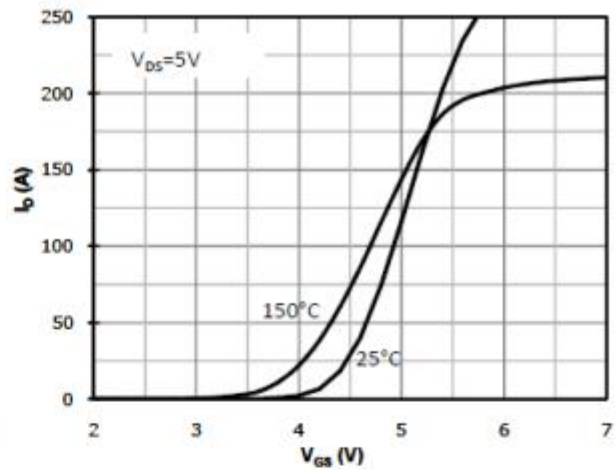


Fig 3: Rds(on) vs Drain Current and Gate Voltage

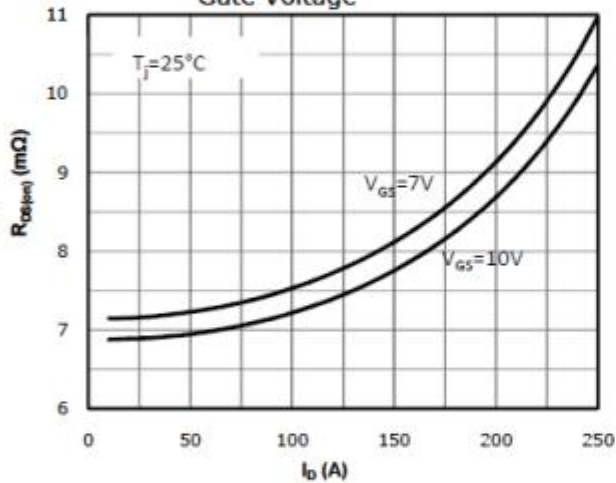


Fig 4: Rds(on) vs Gate Voltage

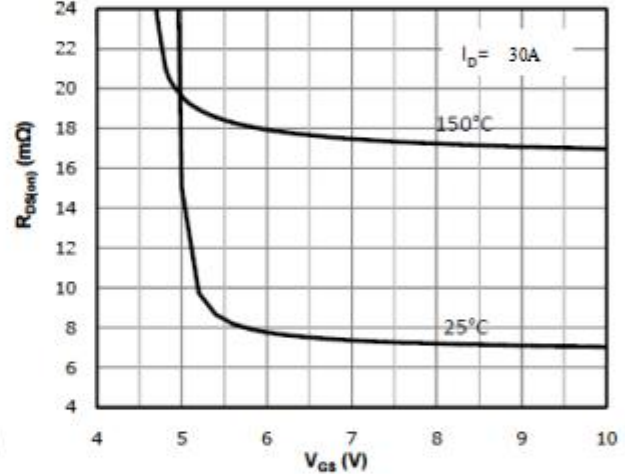


Fig 5: Rds(on) vs. Temperature

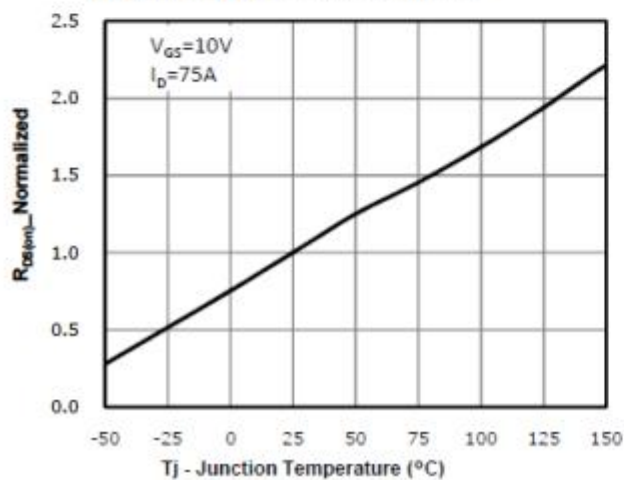


Fig 6: Capacitance Characteristics

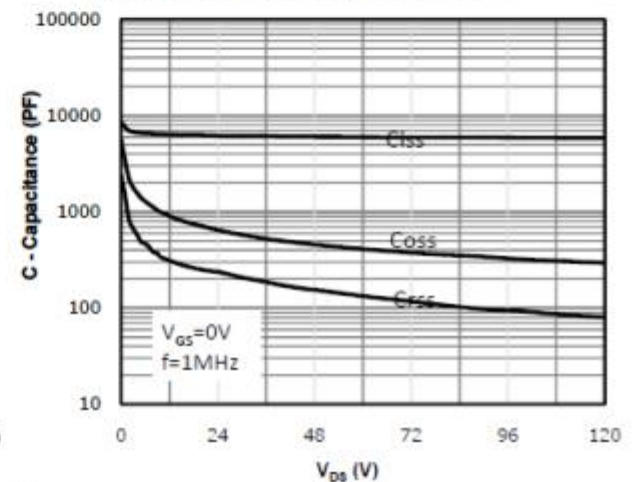


Fig 7: Gate Charge Characteristics

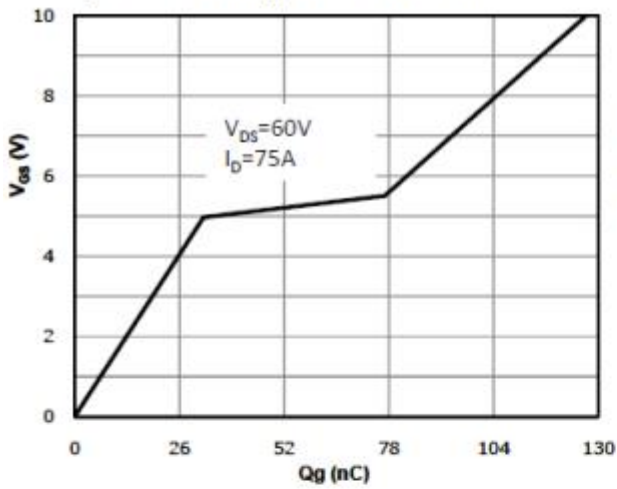


Fig 8: Body-diode Forward Characteristics

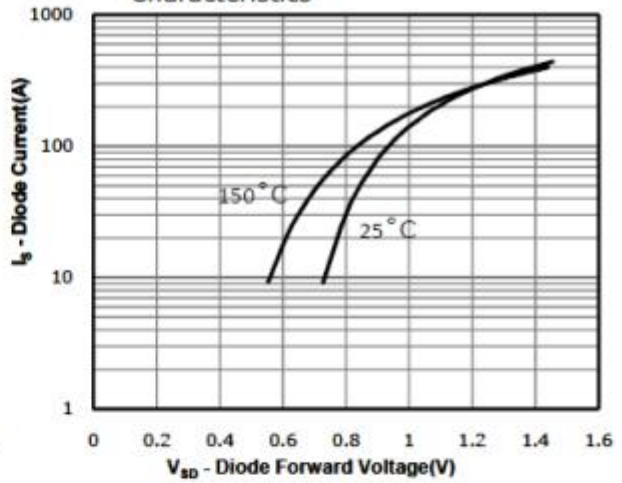


Fig 9: Power Dissipation

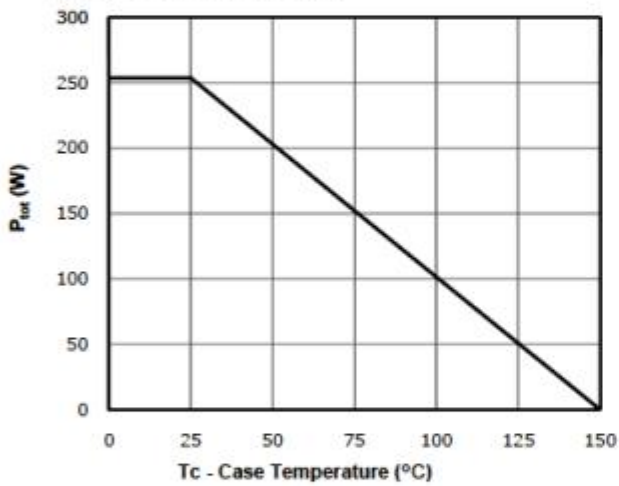


Fig 10: Drain Current Derating

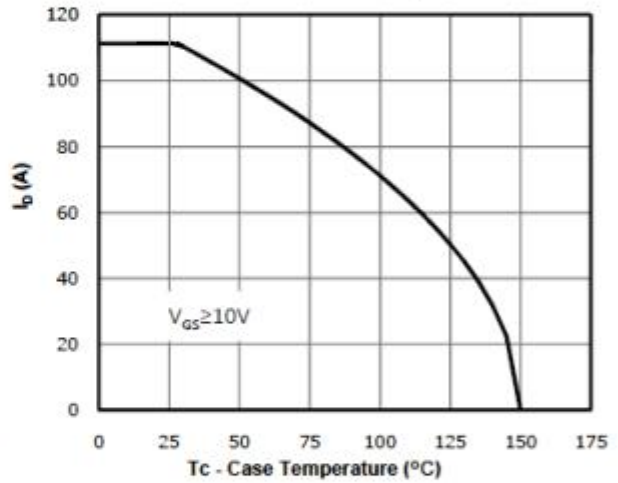


Fig 11: Safe Operating Area

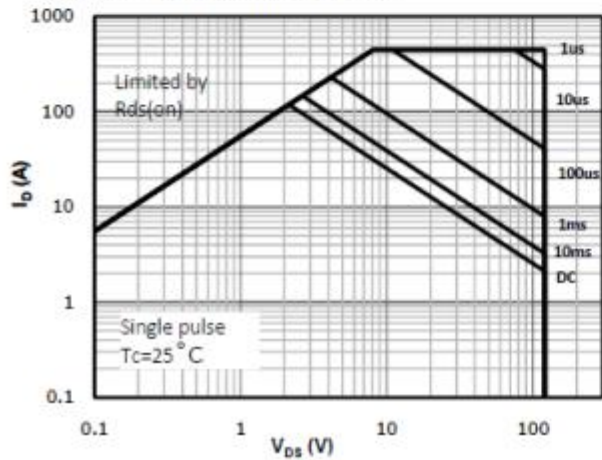


Fig 12: Max. Transient Thermal Impedance

